**Code Coverage Result**

| **Design** | **Code Coverage**  **(Trojan Free)** | **Code Coverage**  **(TIn but not Triggered)** | **Code Coverage**  **(TIn but Triggered)** |
| --- | --- | --- | --- |
| RS232-T100 | 94.45% | 93.57% | 94.06% |
| RS232-T200 | 94.45% | 88.55% |  |
| RS232-T300 | 94.45% | 92.35% |  |
| RS232-T400 | 94.45% | 81.36% |  |
| RS232-T500 | 94.45% | 79.99% |  |
| RS232-T600 | 94.45% | 72.53% |  |
| RS232-T700 | 94.45% | 51.58% |  |
| RS232-T800 | 94.45% | 43.05% |  |
| RS232-900 | 94.45% | 43.05% |  |
| RC6 | 100% | 99.80% | 99.99% |
| Spi\_master | 66.68% | 58.50% |  |
| syncRAM | 79.27% | 73.80% | 78.57% |
| Det\_1011 | 95.18% | 94.55% |  |
| RC5 | 100% | NA |  |
| xtea | 98.96% | NA |  |
| vga | Only one input |  |  |